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μ OBERON

**A Development System
for Mcs51 Microcontrollers**

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Kurzfassung

Als aussergewöhnlich vielseitig einsetzbare Bauteile gewinnen Mikrocontroller immer mehr an Bedeutung. Verglichen mit mechanischen oder elektrischen Systemen erhöhen sie Funktionalität und Zuverlässigkeit und reduzieren gleichzeitig Grösse und Kosten. Zudem trägt die Wiederverwendbarkeit von Programm- und Bauteilen zusätzlich zur Reduktion der Entwicklungszeit und Kosten bei.

Mikrocontroller werden heutzutage aus Gründen der Geschwindigkeit häufig in Assembler und nicht in einer Hochsprache programmiert. In Assembler erstellte Programme weisen meist eine höhere Codedichte auf, was einen geringeren Speicherbedarf und eine kürzere Ausführungszeit zur Folge hat. Auf der anderen Seite bietet eine Hochsprache mehr Komfort und Effizienz beim Entwickeln von Programmen. Modulares Programmieren und Wiederverwendbarkeit von Programmteilen sind nur zwei Stichworte in diesem Zusammenhang.

μ Oberon ist ein Versuch, die Geschwindigkeit von Assembler mit der Modularität von Hochsprachen zu vereinen. Mit dem Inline-Assembler ist es möglich, neben den Anweisungen in der Hochsprache direkt auf Maschinenebene zu programmieren, wo alle Register des Mikrocontrollers direkt zugänglich sind.

Die Hauptschwierigkeit während der Entwicklung von μ Oberon lag darin, dass der Instruktionssatz der Mcs51-Mikrocontroller keine Befehle mit indirekter Adressierung mit Offset anbietet. Andere Crosscompiler berechnen daher die Zieladresse bei jedem Variablenzugriff, so dass wie gebräuchlich die lokalen Variablen auf dem Stack alloziert werden können. μ Oberon hingegen führt mit der statischen Speicherzuteilung einen neuen Ansatz ein. Die absoluten Adressen der lokalen Variablen werden hier statisch während dem Linken zugeteilt, was zu besserem Code führt, der sich selbst mit Assemblerprogrammen vergleichen lässt.

Abstract

Providing general purpose solutions, microcontrollers are becoming increasingly important in the world of electronic systems. Compared to mechanical or simple analog electrical control systems they dramatically improve functionality and reliability, while reducing size and cost. Furthermore the capability of reusing software and hardware components reduces overall design-in time and cost.

Nowadays microcontrollers usually are programmed in assembler and not in a high-level language for efficiency reasons. As a rule, programs written in assembler language have a higher code density, what results in a lower memory consumption and a shorter execution time. On the other side a high-level language provides more comfort and higher efficiency during program development. Modular programming and code reusing are only two points in that context.

μ Oberon is a an approach to unite the speed of assembler programs and the modularity of high-level programs. It allows high-level as well as low-level programming, i.e. all microcontroller resources are easily accessible. This implied the need of an inline assembler, and μ Oberon offers a comfortable embedding of assembler sections into the language.

The main problem while developing μ Oberon was the fact that Mcs51 microcontrollers don't provide indirect-offset addressing. Therefore other cross compilers calculate the effective address on each variable access, so local variables may be located on the stack and addressed relative to a frame pointer as usual. μ Oberon on the other hand introduces a new approach, the so-called Static Memory Assignment, where the absolute addresses of local variables are assigned statically while linking. This leads to better code that even competes with assembler programs.

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